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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
EXPEDITED PROCEDURE PURSUANT
TO 37 CFR §1.116 REQUESTED
Attn: Mail Stop AF

Applicants: Atsushi SHIMODA et al.
Serial No.: 09/783,604
Filed: February 15, 2001
For: METHOD FOR ANALYZING CIRCUIT PATTERN DEFECTS
AND A SYSTEM THEREOF
Group: 2623
Examiner: Virginia M. Kibler

AMENDMENT AFTER FINAL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 15, 2004

Sir.

In response to the Office Action (Paper No. 8) dated on July 14, 2004, please
amend the above-identified application as follows.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 11 of this paper.